

## Gokaraju Rangaraju Institute of Engineering and Technology (Autonomous)

**GR11** Regulations

## GRIET/DOE/3H/G/18-19 M.Tech. I Year I Semester Supplementary Examinations (GR11) Time Table Dec/Jan 2018

TIME: 10.00 A.M TO 1.00 P.M

TIME: 10.00 A.M T								
Date	31/12/2018	02/01/2019	04/01/2019	07/01/2019	09/01/2019	11/01/2019	17/01/2019	
Branch	(Monday)	(Wednesday)	(Friday)	(Monday)	(Wednesday)	(Friday)	(Thursday)	
TE (21)	GR11D5131 Advanced Thermodynamics	GR11D5114 Advanced Fluid Mechanics	GR11D5132 Advanced Heat and Mass Transfer	GR11D5147 Theory of Heat Pipes	GR11D5124 Pressure Vessels and Piping (Elective-I)	GR11D5145 Material Science and Technology (Elective-II)		
SE (25)	GR11D5028 Software Requirements and Estimation	GR11D5036 Distributed Databases (Elective-II)	GR11D5030 Software Process and Project Management	-	GR11D5032 Data warehousing and Data Mining (Common to SE & CSE) (Elective-I)	GR11D5031 Java and Web Technologies (Common to SE & CSE)	GR11D5029 Object Oriented Modeling (Common to SE & CSE)	
PE (43)	GR11D5051 Analysis of Power Electronic Converters	GR11D5053 Power Electronic Control of DC Drives	GR11D5050 Machine Modeling and Analysis	GR11D5056 Modern Power Electronics (Elective-I)	GR11D5057 PLCs and their Applications (Elective-II)	GR11D5052 Modern Control Theory	_	
DFM (52)	GR11D5078 Advanced CAD (Elective-II)	GR11D5075 Special Manufacturing Process (Elective-I)	GR11D5072 Materials Technology	GR11D5073 Precision Engineering	GR11D5071 Advanced Mechanics of Solids	GR11D5074 Design for Manufacturing and Assembly	-	
ES (55)	GR11D5004 Advanced Computer Communication Networks	GR11D5001 Microcontrollers for Embedded System Design (Common to ES & VLSI)	GR11D5008 Digital System Design (Common to ES & VLSI) (Elective-II)	GR11D5002 Embedded Real Time Operating Systems	-	GR11D5007 Advanced Digital Signal processing (Common to ES & VLSI) (Elective-I)	GR11D5003 VLSI Technolog and Design (Common to ES & VLSI)	
VLSI (57)	GR11D5093 Algorithms for VLSI Design Automation	GR11D5001 Microcontrollers for Embedded System Design (Common to VLSI& ES)	GR11D5008 Digital System Design (Common to VLSI& ES) (Elective-I)	GR11D5016 CPLD and FPGA Architectures and Applications	-	GR11D5007 Advanced Digital Signal Processing (Common to VLSI& ES) (Elective-II)	GR11D5003 VLSI Technology and Design (Common to VLSI& ES)	

Date	31/12/2018	02/01/2019	04/01/2019	07/01/2019	09/01/2019	11/01/2019	17/01/2019
Branch	(Monday)	(Wednesday)	(Friday)	(Monday)	(Wednesday)	(Friday)	(Thursday)
CSE (58)	-	GR11D5031 Java and Web Technologies (Common to CSE & SE)	GR11D5102 Embedded Systems and Programming	GR11D5103 Computer System Design (Elective-II)	GR11D5032  Data Warehousing and Data Mining (Common to CSE & SE) (Elective-I)	GR11D5101 Advanced Problem Solving	GR11D5029 Object Oriented Modeling (Common to CSE & SE)

Note: Any discrepancy may please be informed to DOE immediately.

Date: 22/12/2018

DOF